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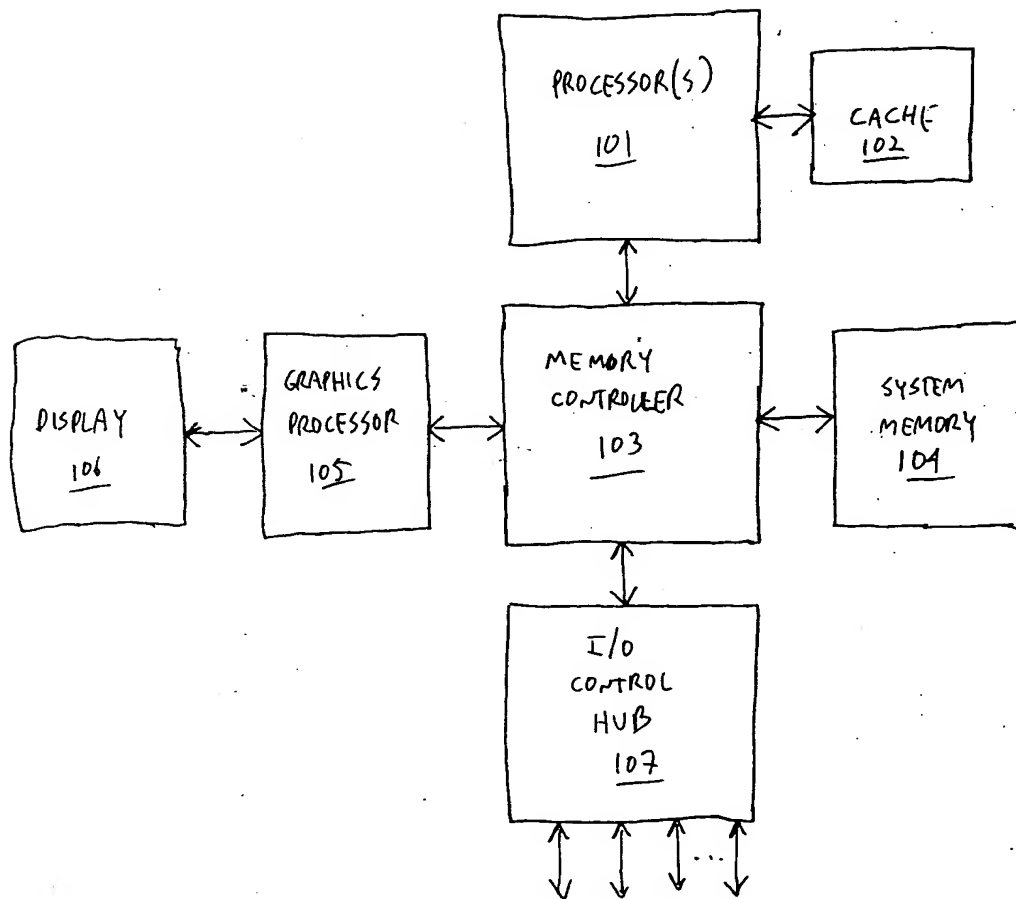


FIG. 1
(PRIOR ART)

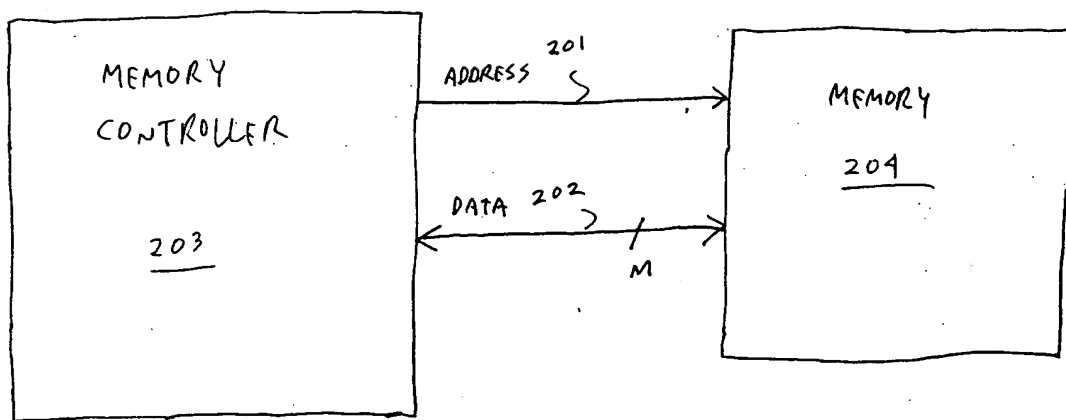
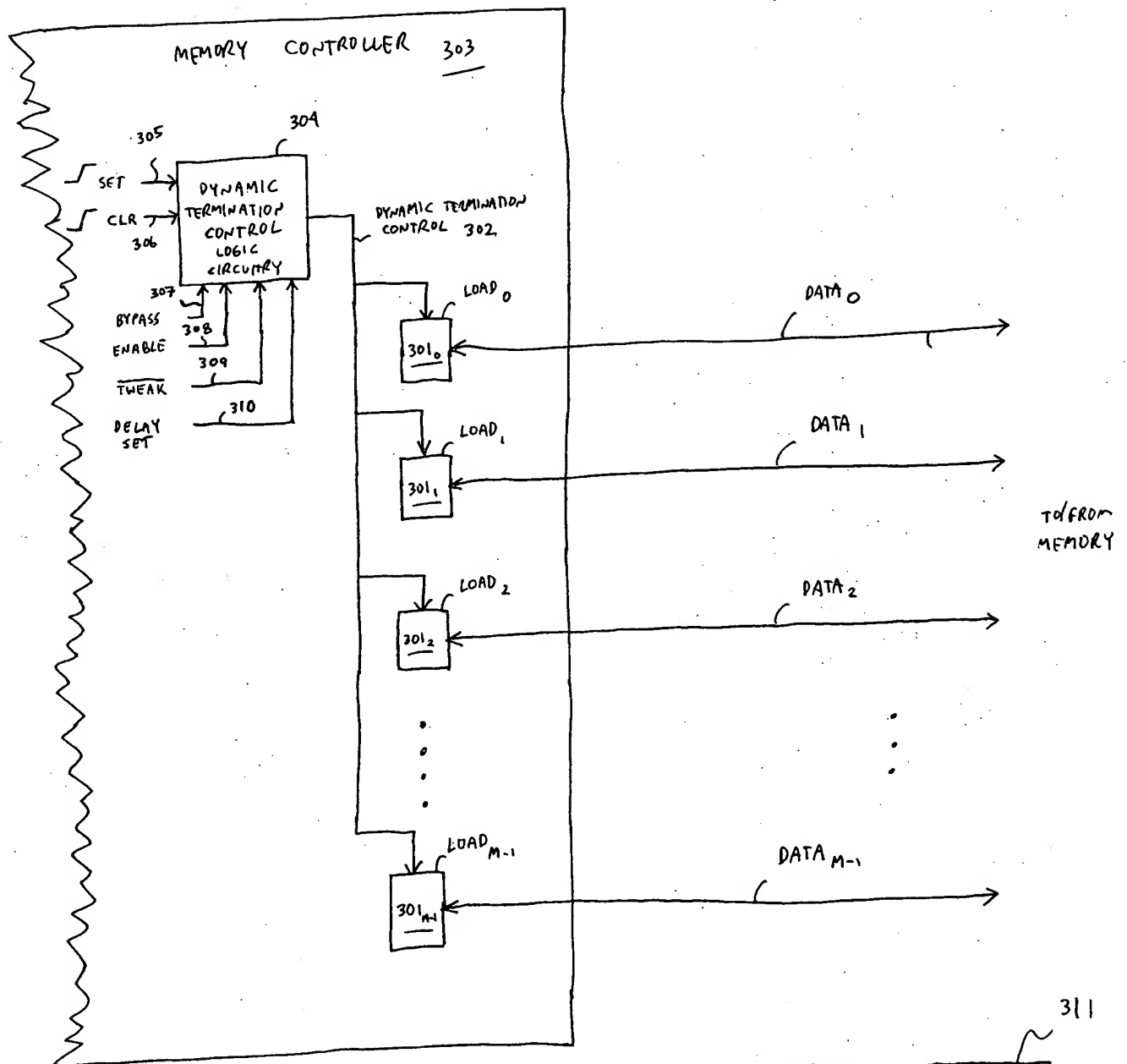
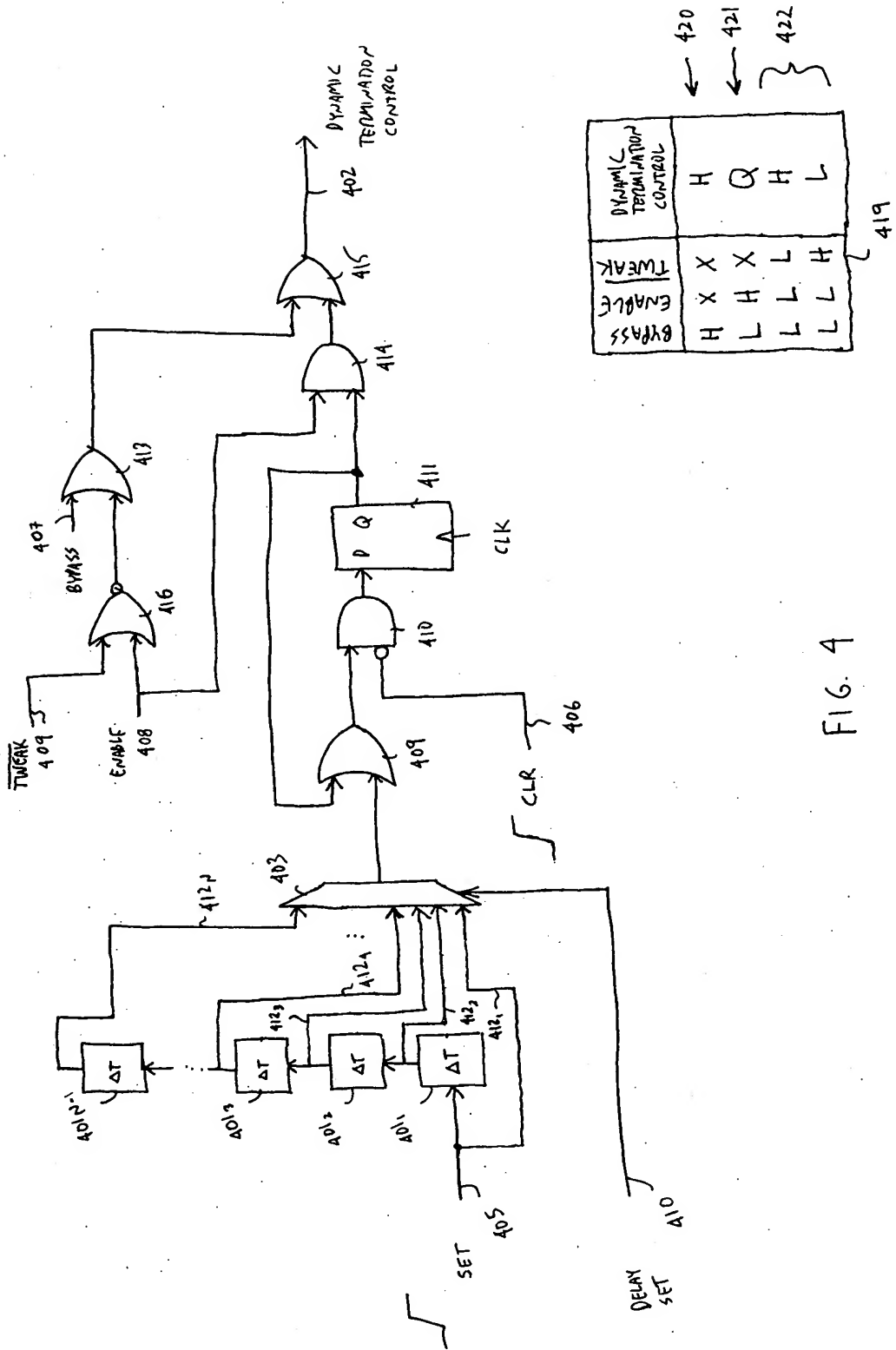


FIG. 2
(PRIOR ART)



MEMORY FUNCTION	WRITE	READ
LOAD IMPEDANCE ST.	INACTIVE	ACTIVE

FIG. 3.



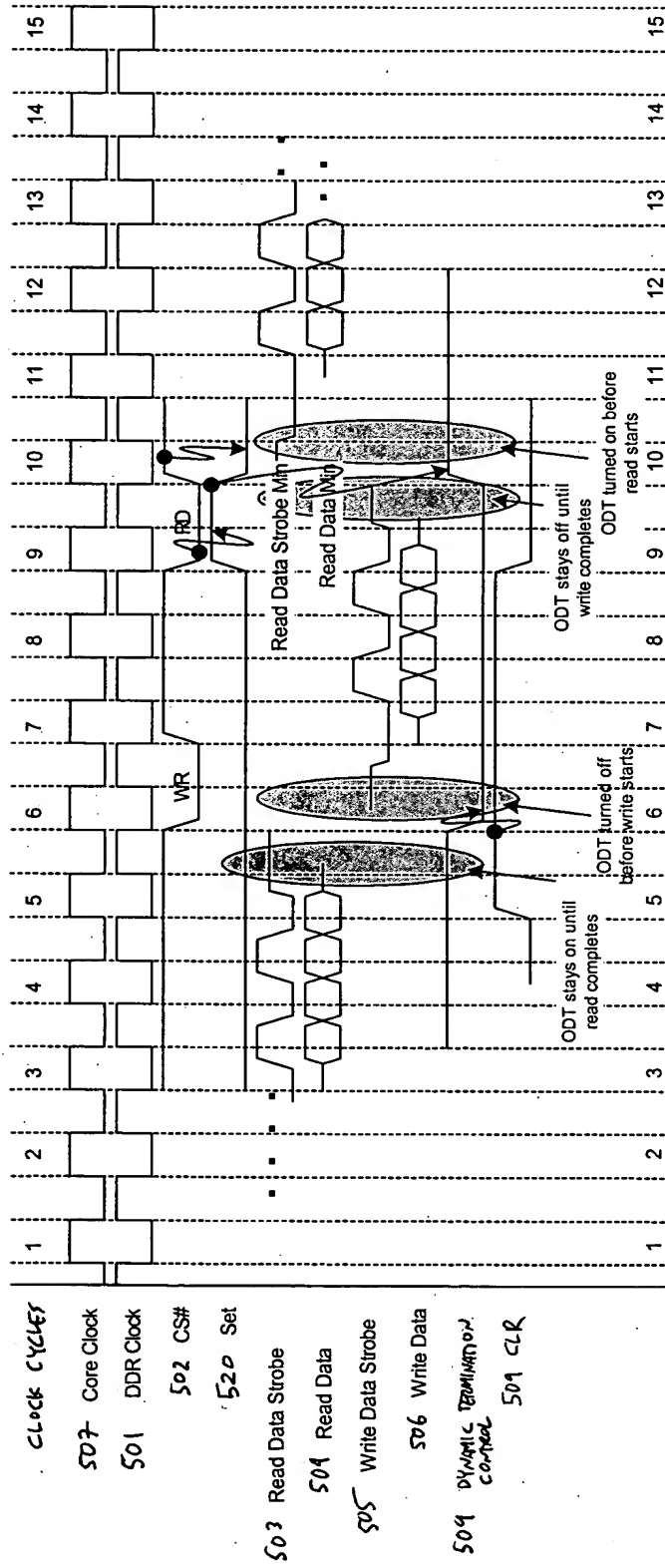


FIG. 5

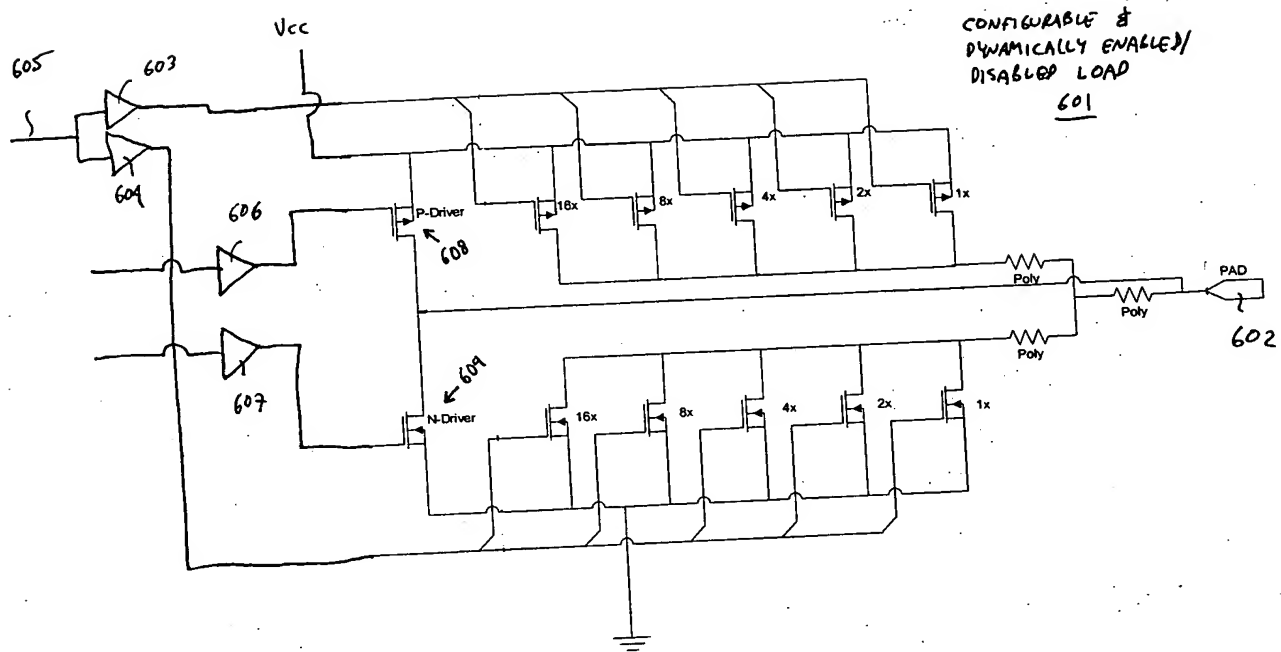


FIG. 6